IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned Examiner: Unassigned

INFORMATION

DISCLOSURE

STATEMENT

In Re PATENT APPLICATION Of:

Applicants: Mitsuhiko OGIHARA et al.

Serial No. : Unassigned

Filed: November 13, 2003

For : COMBINED SEMICONDUCTOR

APPARATUS WITH THIN SEMICONDUCTOR FILMS

Attorney Ref. : MAE 300

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached is a copy of a Japanese publication. Any relevance of the Japanese publication can be gleaned from the attached English-language Abstract, and from page 2 of the present application, where this document is discussed. The document is listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,

November 13, 2003

Date

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			Applicant				
				Mitsuhiko OGIHARA et al.			
				Filing Date	Group Unit		
				November 13, 2003	To Be Assigned		
U.S. PATENT DOCUMENTS							
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FOREIGN PATENT DOCUMENTS							
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							